

What is Claimed is:

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1. A television signal processor for processing a received broadcast wave and generating a television signal, comprising:
storage means for storing video data and additional information separated from the received broadcast wave and OSD data generated on a receiver;

read means for respectively reading the video data, the additional information and the OSD data from said storage means;

standard detection means for detecting a standard of the received broadcast wave;

timing control means for respectively controlling the timing of said read means for reading the video data, the OSD data and the additional information from said storage means in correspondence to the standard detected by said standard detection means; and

combining means for combining the video data, the OSD data and the additional information read by said read means to output the combined data as a television signal.

2. The television signal processor according to claim 1, wherein said broadcast wave is a digital broadcast wave.

3. The television signal processor according to claim 1, wherein said timing control means includes:

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memory means for storing timing information for defining the read timing of said read means according to standard of the broadcast wave; and

reference means for referring to said memory means for timing information corresponding to the standard detected by said standard detection means, and supply the same to said read means, wherein

said read means respectively reads the video data, the additional information, and the OSD data from said storage means at timing corresponding to the timing information supplied from said reference means.

4. The television signal processor according to claim 3, wherein said memory means comprises:

a first table memory stored timing information for defining read timing for video data and OSD data according to standard of broadcast wave; and

a second table memory stored timing information for defining read timing for additional information according to standard of broadcast wave and information in the additional information, wherein

said reference means refers to said first table memory thereby providing said read means with the timing information for defining the read timing for the video data and the OSD data, and refers to said second table memory thereby providing said read

means with the timing information for defining the read timing for the additional information.

5. The television signal processor according to claim 1, further comprising level conversion means for converting an output level of the additional information read by said read means, wherein said combining means combines the additional information whose output level was converted by said level conversion means into the video data and the OSD data read by said read means.

6. The television signal processor according to claim 5, wherein said level conversion means converts the output level of the additional information into a level determined according to the standard detected by said standard detection means.